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To: Examiner David Lipscomb, **From:** Larry E. Henneman, Jr.
US Patent and Trademark Office

Fax: (571) 270-9988

Pages: 18, including cover sheet

Phone:

Date: January 31, 2007

Re: Power of Attorney

CC:

☐ Urgent ☐ For Review ☐ Please Comment ☒ Please Confirm Receipt

• **ATTENTION:** This facsimile transmission may contain confidential attorney-client communication and/or attorney work-product. If you receive this transmission in error, please take necessary measures to maintain its confidentiality, and notify the sender immediately.

Applicant(s): Moore (et al.)
Serial No.: 08/484,918
Filed: June 7, 1995

Attorney Docket: 0081-011D3
Examiner: David Y. Eng
Group Art Unit: 2315

Title: High Performance Microprocessor Having Variable Speed System Clock

The following are copies of documents originally transmitted on June 27, 2006, which are being re-sent for filing in the above referenced application. The entire filing is being included for completeness, however it is the Power of Attorney documents that are the focus of this re-submission:

1. Request For Certificate of Correction of Patent For Applicants' Mistake (4 pages);
2. Form PTO/SB/44 in duplicate;
3. Power of Attorney from Technology Properties Limited (copy, 1 page);
4. Certificate Under 37 CFR § 3.73(b) from Technology Properties Limited (1 page);
5. Power of Attorney from Patriot Scientific Corporation (copy, 1 page);
6. Certificate Under 37 CFR § 3.73(b) from Patriot Scientific Corporation (1 page);
7. Power of Attorney from Charles H. Moore (1 page);
8. Certificate Under 37 CFR § 3.73(b) from Charles H. Moore (1 page);
9. Credit Card Payment Form PTO-2038 for \$100.00;
10. Return Receipt Postcard; and
11. Returned Postcard with United States Patent Office date stamp.

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

Patent No. 5,809,336
Issue Date: September 15, 1998
Application No.: 08/484,918
Applicant(s): Moore, (et al.)
Title: High Performance Microprocessor Having Variable Speed System Clock
Docket No.: 0081-011D3

Director
Technology Center 2180
U.S. Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

CUSTOMER NO.: 40972

TRANSMITTAL OF REQUEST FOR CERTIFICATE OF CORRECTION
OF PATENT FOR APPLICANTS' MISTAKE (37 CFR § 1.323)

Enclosed are the following documents for filing in the above referenced issued patent:

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Respectfully submitted,

Dated: 6/27/06
Henneman & Associates, PLC
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Larry E. Henneman, Jr.
Larry E. Henneman, Jr.
Reg. No. 41,063

CERTIFICATE OF MAILING (37 CFR 1.8(A))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Director, Technology Center 2180, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: 6/27/06

Larry E. Henneman, Jr.
Larry E. Henneman, Jr.

U. S. Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450



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THE STAMP OF THE UNITED STATES PATENT AND TRADEMARK OFFICE
HEREON ACKNOWLEDGES RECEIPT OF THE FOLLOWING:

Inventors(s): Moore (et al.)

Issue Date: September 15, 1998

Patent No: 5,809,336

Atty. Docket No: 0081-011D3

App. Serial No.: 08/484,918

Title: High Performance Microprocessor Having Variable Speed System Clock

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Date: June 27, 2006

Initials: LEH:pgg

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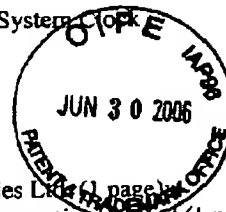
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